UNIVERSITY INSTITUTE OF ENGINEERING & TECHNOLOGY PANJAB UNIVERSITY, CHANDIGARH - 160014

FACULTY PROFILE

1. Name : CHARU

2. Designation : ASSISTANT PROFESSOR

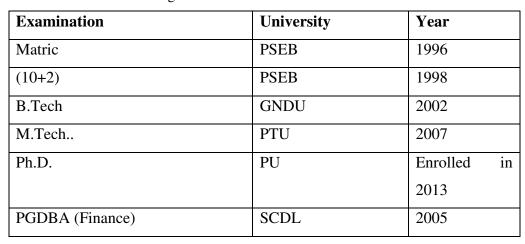
3. Department : Electronics & Communication Engineering

4. Contact No. : 8146560572

5. Email id : charu_uiet@pu.ac.in

6. Research Areas : Nanophotonics, Optical communication

7. Educational Background :



8. Experience:

Institution	Designation			Date of Joining	Date of Leaving
		Duties			
GNDU	Lecturer	Teaching an	d	26-7-2004	13-8-2007
Regional		research			
Campus,					
Jalandhar					
GNDU	Lecturer	Teaching an	d	18-7-2002	30-4-2003
Regional		research			
Campus,					
Jalandhar					
GNDU	Lecturer	Teaching an	d	18-7-2003	30-4-2004
Regional		research			
Campus,					
Jalandhar					



9. List of Publications:

- 1. Vivek Gupta, Charu Madhu, "Development of Test-Bed for Gain Improvement of Erbium Doped Fiber Amplifiers", *International Journal of Computer Science and Technology*, Vol.1, 2010, 100-103.
- 2. Charu Narula, Jaswinder Singh, "Simulative analysis of soliton based DPSK transmission system", *Optik, Elsevier*, Vol. 122, 2011, 941-944.
- 3. Deepika Setia , Charu Madhu, "High Speed Parallel Multiplier –Accumulator (MAC)-A Review", *International Journal of Application or Innovation in Engineering & Management*, Vol.2, 2013, 222-228.
- 4. Harmeet Kaur, Charu Madhu, "Montgomery Multiplication Methods A Review", *International Journal of Application or Innovation in Engineering & Management*, Vol.2, 2013, 229-235.
- 5. Charu Madhu, Preeti Singh, "Transmission Performance Of Soliton WDM Signals: A Review", *International Journal of Engineering Research & Technology*, Vol.2, 2013, 1-4.
- 6. Deepika Setia, Charu Madhu, "Novel Architecture of High speed parallel MAC using Carry Select Adder", *International Journal of Computer Applications*, Vol. 74, 2013, 32-38.
- 7. Harmeet Kaur, Charu Madhu, "Optimized multiple Word Radix-2 Montgomery Multiplication Algorithm", *International Journal of Engineering Trends and Technology*, Vol. 4, 2013, 2836-2841.
- 8. Aastha Singhal, Charu Madhu, Vijay kumar, "Designs of All-Digital Phase Locked Loop", IEEE Conference, RAECS 2014.
- 9. Anchal Sharma, Charu Madhu, Jatinder Singh, "Performance Evaluation of Thin Film Transistors: History, Technology development and Comparison", *International Journal of Computer Applications*, Vol. 89, 2014, 36-40.
- 10. Yogita Bansal, Charu Madhu, Pardeep Kaur, "High Speed Vedic Multiplier Designsa Review", IEEE Conference, RAECS 2014.