

Timetable For BE Electronics and Communications (Sec-I) (3rd Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-221, Block II	<u>microprocessor and micro-controller</u> Vishal Sharma Room - ECE LH-221, Block II	<u>Analog Electronic Circuits</u> Arvind Kumar Room - ECE LH-221, Block II		<u>Electronic Measurements and instrumentation</u> Sarvjit Singh Room - ECE LH-221, Block II	<u>signal and system</u> Group 1 Neeraj Sharma Room - ECE LH-221, Block II	<u>Linear Algebra and Complex Analysis</u> Group 1 Saurabh Bhatia Room - ECE LH-220 Block II
								<u>signal and system</u> Group 2 Neeraj Sharma Room - ECE LH-221, Block II
Tuesday		<u>microprocessor and micro-controller(P)</u> Group 1 Nidhi Garg Room - ECE Lab 213 Block II	<u>signal and system</u> Neeraj Sharma Room - ECE LH-221, Block II	<u>Electronic Measurements and instrumentation</u> Sanjeev Kumar Room - ECE LH-221, Block II		<u>Economics</u> Raman Room - ECE LH-221, Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-221, Block II	<u>Linear Algebra and Complex Analysis</u> Group 2 Saurabh Bhatia Room - ECE LH-221, Block II
		<u>Analog Electronic Circuits (P)</u> Group 2 Arvind Kumar Room - ECE Lab 209 Block II						
		<u>Electronic Measurements and instrumentation(P)</u> Group 3 Sarvjit Singh Room - ECE Lab 311 Block II						
Wednesday	<u>microprocessor and micro-controller</u> Vishal Sharma Room - ECE LH-221, Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-221, Block II	<u>Analog Electronic Circuits</u> Arvind Kumar Room - ECE LH-221, Block II	<u>Electronic Measurements and instrumentation</u> Sanjeev Kumar Room - ECE LH-221, Block II		<u>Analog Electronic Circuits (P)</u> Group 1 Arvind Kumar Room - ECE Lab 209 Block II		<u>Linear Algebra and Complex Analysis</u> Group 3 Saurabh Bhatia Room - ECE LH-221, Block II
						<u>Electronic Measurements and instrumentation(P)</u> Group 2 Sarvjit Singh Room - ECE Lab 311 Block II		
						<u>microprocessor and micro-controller(P)</u> Group 3 Nidhi Garg Room - ECE Lab 213 Block II		
Thursday	<u>Economics</u> Raman Room - ECE LH-221, Block II	<u>signal and system</u> Neeraj Sharma Room - ECE LH-221, Block II	<u>Electronic Measurements and instrumentation(P)</u> Group 1 Sarvjit Singh Room - ECE Lab 311 Block II			<u>Analog Electronic Circuits</u> Arvind Kumar Room - ECE LH-221, Block II	<u>microprocessor and micro-controller</u> Vishal Sharma Room - ECE LH-221, Block II	
			<u>microprocessor and micro-controller(P)</u> Group 2 Nidhi Garg Room - ECE Lab 213 Block II					
			<u>Analog Electronic Circuits (P)</u> Group 3 Charu Madhu Room - ECE Lab 209 Block II					
Friday	<u>Economics</u> Raman Room - ECE LH-221, Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-221, Block II	<u>microprocessor and micro-controller</u> Vishal Sharma Room - ECE LH-221, Block II	<u>signal and system</u> Neeraj Sharma Room - ECE LH-221, Block II		<u>signal and system</u> Group 3 Neeraj Sharma Room - ECE LH-220 Block II		

Timetable Incharge

Timetable For BE Electronics and Communications (Sec-I) (5th Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday	<u>DSD</u> Sharmelee Thangjam Room - ECE LH-219 Block II	<u>VLSI DESIGN</u> <u>B.E</u> Garima Room - ECE LH-219 Block II	<u>DSP(P)</u> Group 1 Sunil Agrawal Room - ECE LAB 414A Block II			<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-219 Block II		
			<u>VLSI DESIGN (P)</u> Group 2 Garima Room - ECE Lab 314 Block II					
			<u>DSD(P)</u> Group 3 Sharmelee Thangjam Room - ECE Lab 311 Block II					
Tuesday	<u>DSD</u> Sharmelee Thangjam Room - ECE LH-219 Block II	<u>DSP</u> Sunil Agrawal Room - ECE LH-219 Block II	<u>AWP</u> Sarvjit Singh Room - ECE LH-219 Block II	<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-219 Block II		<u>Data Structures and Algorithms</u> Preeti Gupta Room - ECE LH-219 Block II	<u>DSD</u> Group 1 Vishal Sharma	
						<u>DSP</u> Group 3 Sunil Agrawal Room - ECE LH-219 Block II		
Wednesday	<u>VLSI DESIGN (P)</u> Group 1 Garima Room - ECE Lab 314 Block II		<u>DSD</u> Sharmelee Thangjam Room - ECE LH-219 Block II	<u>Data Structures and Algorithms</u> Preeti Gupta Room - ECE LH-219 Block II		<u>DSP</u> Group 1 Sunil Agrawal Room - ECE LH-219 Block II		
	<u>DSD(P)</u> Group 2 Sharmelee Thangjam Room - ECE Lab 311 Block II					<u>DSD</u> Group 2 Sharmelee Thangjam		
	<u>DSP(P)</u> Group 3 Sunil Agrawal Room - ECE LAB 414A Block II							
Thursday	<u>AWP</u> Sarvjit Singh Room - ECE LH-219 Block II	<u>DSP</u> Sunil Agrawal Room - ECE LH-219 Block II	<u>VLSI DESIGN</u> <u>B.E</u> Garima Room - ECE LH-219 Block II	<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-219 Block II		<u>DSD(P)</u> Group 1 Sharmelee Thangjam Room - ECE Lab 311 Block II		
						<u>DSP(P)</u> Group 2 Daljeet Kaur Room - ECE LAB 414A Block II		
						<u>VLSI DESIGN (P)</u> Group 3 Garima Room - ECE Lab 314 Block II		
Friday	<u>AWP</u> Sarvjit Singh Room - ECE LH-219 Block II	<u>DSP</u> Sunil Agrawal Room - ECE LH-219 Block II	<u>VLSI DESIGN</u> <u>B.E</u> Garima Room - ECE LH-219 Block II	<u>Data Structures and Algorithms</u> Preeti Gupta Room - ECE LH-219 Block II		<u>DSP</u> Group 2 Sunil Agrawal Room - ECE LH-219 Block II		
						<u>DSD</u> Group 3 Sharmelee Thangjam		

Timetable Incharge

Timetable For BE Electronics and Communications (Sec-I) (7th Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		WC Group 1 Naresh Kumar Room - ECE LH-220 Block II	WC Naresh Kumar Room - ECE LH-219 Block II	Computer Architecture and Organisation Nidhi Garg Room - ECE LH-219 Block II		Operating Systems Pardeep Kaur Room - ECE LH-201, Block II	Embedded System Design Harvinder Kaur Room - ECE LH-201, Block II	
Tuesday		Operating Systems Pardeep Kaur Room - ECE LH-218 block II	Embedded System Design Harvinder Kaur Room - ECE LH-201, Block II	Computer Architecture and Organisation Nidhi Garg Room - ECE LH-201, Block II		WC(P) Group 1 Naresh Kumar Room - ECE Lab 212 Block II		
Wednesday	Project 1(ECE) Group 1 Nidhi Garg	Computer Architecture and Organisation Nidhi Garg Room - ECE LH-218 block II	Embedded System Design Harvinder Kaur Room - ECE LH-218 block II	WC Naresh Kumar Room - ECE LH-218 block II		Seminar 1(ECE) Group 1 Vishal Sharma		
Thursday	Project 1(ECE) Group 2 Amit Chaudhary	Computer Architecture and Organisation Nidhi Garg Room - ECE LH-218 block II	Embedded System Design (P) Group 1 Harvinder Kaur Room - ECE Lab 216 Block II		Operating Systems Pardeep Kaur Room - ECE LH-219 Block II	Project 1(ECE) Group 1 Sanjeev Kumar		
Friday	Project 1(ECE) Group 3 Naresh Kumar	Embedded System Design Harvinder Kaur Room - ECE LH-201, Block II	Operating Systems Pardeep Kaur	WC Group 2 Arvind Kumar Room - ECE Lab 213 Block II		WC Naresh Kumar Room - ECE LH-218 block II	Project 1(ECE) Group 2 Harvinder Kaur	
				WC Group 3 Naresh Kumar Room - ECE Lab 209 Block II			Project 1(ECE) Group 3 Sarvjit Singh	
							Seminar 1(ECE) Group 3 Vijay Kumar	

Timetable Incharge

Timetable For BE Electronics and Communications (Sec-II) (3rd Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday	<u>Analog Electronic Circuits (P)</u> Group 4 Charu Madhu Room - ECE Lab 209 Block II		<u>Economics Raman</u> Room - ECE LH-220 Block II	<u>Analog Electronic Circuits</u> Charu Madhu Room - ECE LH-220 Block II		<u>microprocessor and micro-controller</u> Harvinder Kaur Room - ECE LH-220 Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-220 Block II	<u>signal and system</u> Group 5 Gurpreet Kaur ECE
	<u>Electronic Measurements and instrumentation(P)</u> Group 5 Sarvjit Singh Room - ECE Lab 311 Block II							
	<u>microprocessor and micro-controller(P)</u> Group 6 Vishal Sharma Room - ECE Lab 213 Block II							
Tuesday	<u>Economics Raman</u> Room - ECE LH-220 Block II	<u>Analog Electronic Circuits</u> Charu Madhu Room - ECE LH-220 Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-220 Block II	<u>signal and system</u> Gurpreet Kaur ECE Room - ECE LH-220 Block II		<u>microprocessor and micro-controller(P)</u> Group 4 Vishal Sharma Room - ECE Lab 213 Block II		
Wednesday	<u>Economics Raman</u> Room - ECE LH-220 Block II	<u>Analog Electronic Circuits</u> Charu Madhu Room - ECE LH-220 Block II	<u>Electronic Measurements and instrumentation</u> Garima Room - ECE LH-220 Block II	<u>microprocessor and micro-controller</u> Harvinder Kaur Room - ECE LH-220 Block II		<u>signal and system</u> Gurpreet Kaur ECE Room - ECE LH-220 Block II	<u>Linear Algebra and Complex Analysis</u> Group 4 Saurabh Bhatia Room - ECE LH-220 Block II	
Thursday	<u>Electronic Measurements and instrumentation</u> Garima Room - ECE LH-220 Block II	<u>microprocessor and micro-controller</u> Harvinder Kaur Room - ECE LH-220 Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-220 Block II	<u>signal and system</u> Gurpreet Kaur ECE Room - ECE LH-220 Block II		<u>Linear Algebra and Complex Analysis</u> Group 6 Saurabh Bhatia Room - ECE LH-220 Block II	<u>signal and system</u> Group 6 Gurpreet Kaur ECE	
Friday	<u>signal and system</u> Group 4 Gurpreet Kaur ECE Room - ECE LH-220 Block II	<u>Electronic Measurements and instrumentation</u> Garima Room - ECE LH-220 Block II	<u>Linear Algebra and Complex Analysis</u> Saurabh Bhatia Room - ECE LH-220 Block II	<u>microprocessor and micro-controller</u> Harvinder Kaur Room - ECE LH-220 Block II		<u>Electronic Measurements and instrumentation(P)</u> Group 4 Gurpreet Kaur ECE Room - ECE Lab 311 Block II		
	<u>Linear Algebra and Complex Analysis</u> Group 5 Saurabh Bhatia Room - ECE LH-218 block II					<u>microprocessor and micro-controller(P)</u> Group 5 Vishal Sharma Room - ECE Lab 213 Block II		
						<u>Analog Electronic Circuits (P)</u> Group 6 Charu Madhu Room - ECE Lab 209 Block II		

Timetable Incharge

Timetable For BE Electronics and Communications (Sec-II) (5th Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday	<u>AWP</u> Jaget Singh Room - ECE LH-201, Block II	<u>DSD</u> Preeti Gupta Room - ECE LH-201, Block II	<u>Data Structures and Algorithms</u> Sarvjit Singh Room - ECE LH-201, Block II	<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-201, Block II		<u>DSD(P)</u> Group 4 Preeti Gupta Room - ECE Lab 311 Block II		
						<u>VLSI DESIGN (P)</u> Group 5 Amit Chaudhary Room - ECE Lab 314 Block II		
						<u>DSP(P)</u> Group 6 Sanjeev Kumar Room - ECE LAB 414A Block II		
Tuesday	<u>VLSI DESIGN</u> <u>B.E</u> Amit Chaudhary Room - ECE LH-201, Block II	<u>AWP</u> Jaget Singh Room - ECE LH-201, Block II	<u>VLSI DESIGN (P)</u> Group 4 Amit Chaudhary Room - ECE Lab 314 Block II			<u>Data Structures and Algorithms</u> Sarvjit Singh Room - ECE LH-201, Block II	<u>DSP</u> Vijay Kumar Room - ECE LH-201, Block II	
			<u>DSP(P)</u> Group 5 Vijay Kumar Room - ECE LAB 414A Block II					
			<u>DSD(P)</u> Group 6 Jaget Singh Room - ECE Lab 311 Block II					
Wednesday	<u>Data Structures and Algorithms</u> Sarvjit Singh Room - ECE LH-201, Block II	<u>VLSI DESIGN</u> <u>B.E</u> Amit Chaudhary Room - ECE LH-201, Block II	<u>DSD</u> Preeti Gupta Room - ECE LH-201, Block II	<u>DSP</u> Vijay Kumar Room - ECE LH-201, Block II		<u>DSD</u> Group 5 Preeti Gupta	<u>DSP</u> Group 6 Vijay Kumar	
Thursday	<u>DSP</u> Group 5 Vijay Kumar	<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-201, Block II	<u>AWP</u> Jaget Singh Room - ECE LH-201, Block II	<u>DSD</u> Preeti Gupta Room - ECE LH-201, Block II		<u>DSD</u> Group 4 Sarvjit Singh	<u>DSP</u> Group 4 Vijay Kumar Room - ECE LH-201, Block II	
	<u>DSD</u> Group 6 Preeti Gupta							
Friday	<u>DSP(P)</u> Group 4 Vijay Kumar Room - ECE LAB 414A Block II		<u>DSP</u> Vijay Kumar Room - ECE LH-201, Block II	<u>Computer Networks</u> Daljeet Kaur Room - ECE LH-201, Block II		<u>VLSI DESIGN</u> <u>B.E</u> Amit Chaudhary Room - ECE LH-201, Block II		
	<u>DSD(P)</u> Group 5 Jaget Singh Room - ECE Lab 311 Block II							
	<u>VLSI DESIGN (P)</u> Group 6 Amit Chaudhary Room - ECE Lab 314 Block II							

Timetable Incharge

Timetable For BE Electronics and Communications (Sec-II) (7th Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday	<u>WC(P)</u> Group 4 Preeti Singh Room - ECE Lab 212 Block II		<u>Operating Systems</u> Pardeep Kaur Room - ECE LH-218 block II	<u>Computer Architecture and Organisation</u> Gurpreet Kaur ECE Room - ECE LH-218 block II		<u>Embedded System Design</u> Vijay Kumar Room - ECE LH-218 block II		
	<u>Embedded System Design (P)</u> Group 6 Pardeep Kaur Room - ECE Lab 216 Block II							
Tuesday	<u>Project 1(ECE)</u> Group 4 Sunil Agrawal	<u>Embedded System Design</u> Vijay Kumar	<u>WC</u> Preeti Singh Room - ECE LH-218 block II	<u>Operating Systems</u> Pardeep Kaur Room - ECE LH-218 block II		<u>WC</u> Group 4 Arvind Kumar	<u>Seminar 1(ECE)</u> Group 5 Neeraj Sharma	
						<u>WC</u> Group 5 Preeti Singh	<u>Seminar 1(ECE)</u> Group 6 Preeti Gupta	
Wednesday	<u>Project 1(ECE)</u> Group 4 Gurpreet Kaur ECE		<u>Embedded System Design (P)</u> Group 4 Pardeep Kaur Room - ECE Lab 216 Block II			<u>Embedded System Design</u> Vijay Kumar Room - ECE LH-218 block II	<u>Computer Architecture and Organisation</u> Gurpreet Kaur ECE Room - ECE LH-218 block II	
	<u>Project 1(ECE)</u> Group 5 Pardeep Kaur		<u>WC(P)</u> Group 5 Preeti Singh Room - ECE Lab 212 Block II					<u>WC</u> Group 6 Preeti Singh
	<u>Project 1(ECE)</u> Group 6 Daljeet Kaur							
Thursday	<u>Seminar 1(ECE)</u> Group 4 Sharmelee Thangjam		<u>WC</u> Preeti Singh Room - ECE LH-218 block II	<u>Embedded System Design</u> Vijay Kumar Room - ECE LH-218 block II		<u>Computer Architecture and Organisation</u> Gurpreet Kaur ECE Room - ECE LH-218 block II	<u>Operating Systems</u> Pardeep Kaur Room - ECE LH-218 block II	
								<u>Project 1(ECE)</u> Group 6 Arvind Kumar
Friday	<u>Project 1(ECE)</u> Group 5 Sumit Budhiraja	<u>WC</u> Preeti Singh Room - ECE LH-218 block II	<u>Computer Architecture and Organisation</u> Gurpreet Kaur ECE Room - ECE LH-218 block II	<u>Operating Systems</u> Pardeep Kaur Room - ECE LH-218 block II		<u>Embedded System Design (P)</u> Group 5 Pardeep Kaur Room - ECE Lab 216 Block II		
						<u>WC(P)</u> Group 6 Daljeet Kaur Room - ECE Lab 212 Block II		

Timetable Incharge

Timetable For M Tech Microelectronics (1st Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		SDP-MTECH Arvind Kumar Room - ECE Lab 314 Block II	MOS INTEGRATED CKT Amit Chaudhary Room - ECE Lab 216 Block II			ICT Vishal Sharma Room - ECE LH-410 Block II	Research seminar MTech Charu Madhu Room - ECE LH-218 block II	
Tuesday	ICT (P) Vishal Sharma Room - ECE Lab 314 Block II		HDL Sharmelee Thangjam Room - ECE Lab 216 Block II	ICT Vishal Sharma Room - ECE Lab 209 Block II		ESD-MTECH Daljeet Kaur Room - ECE LH-218 block II		
Wednesday	MOS INTEGRATED CKT Amit Chaudhary Room - ECE LH-218 block II	SDP-MTECH Arvind Kumar Room - ECE Lab 209 Block II	ESD-MTECH Daljeet Kaur Room - ECE Lab 209 Block II	HDL Sharmelee Thangjam Room - ECE Lab 209 Block II		MOS INTEGRATED CKT(P) Amit Chaudhary Room - ECE Lab 314 Block II		
Thursday	SDP-MTECH (P) Arvind Kumar Room - ECE Lab 314 Block II		ESD-MTECH (P) Pardeep Kaur Room - ECE LAB 414A Block II			ICT Vishal Sharma Room - ECE Lab 209 Block II	MOS INTEGRATED CKT Amit Chaudhary Room - ECE LH-410 Block II	
Friday		ESD-MTECH Daljeet Kaur Room - ECE Lab 216 Block II	HDL(P) Sharmelee Thangjam Room - ECE Lab 314 Block II			SDP-MTECH Arvind Kumar Room - ECE Lab 314 Block II	HDL Sharmelee Thangjam Room - ECE LH-410 Block II	

Timetable Incharge

Timetable For M Tech Microelectronics (3rd Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday			MPT Vijay Kumar	RESEARCH METHODOLOGY Harvinder Kaur				
Tuesday				RESEARCH METHODOLOGY Harvinder Kaur		MPT(P) Amit Chaudhary Room - ECE Lab 314 Block II		
Wednesday			MPT Vijay Kumar	RESEARCH METHODOLOGY Gurpreet Kaur ECE				
Thursday						MPT Vijay Kumar		
Friday		RESEARCH METHODOLOGY Gurpreet Kaur ECE						

Timetable Incharge

Timetable For ME Electronics and Communications (1st Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		<u>DSD -M.E</u> Nidhi Garg Room - ECE LH-410 Block II	<u>FOCS</u> Preeti Singh Room - ECE LH-410 Block II	<u>ADV DC</u> Naresh Kumar Room - ECE LH-410 Block II		<u>FOCS (P)</u> Preeti Singh Room - ECE LAB 414B Block II		
Tuesday		<u>Adv DSP</u> Sumit Budhiraja Room - ECE LH-410 Block II	<u>ADV DC</u> Naresh Kumar Room - ECE LH-410 Block II	<u>INFORMATION THEORY AND CODING</u> Charu Madhu Room - ECE LH-410 Block II		<u>DSD -M.E</u> Nidhi Garg Room - ECE LH-410 Block II	<u>Research Seminar M.E</u> Preeti Singh	
Wednesday	<u>Adv DSP</u> Sumit Budhiraja Room - ECE LH-410 Block II	<u>FOCS</u> Preeti Singh Room - ECE LH-410 Block II	<u>DSD-M.E(P)</u> Nidhi Garg Room - ECE Lab 311 Block II			<u>ADV DC</u> Naresh Kumar Room - ECE LH-410 Block II		
Thursday	<u>ADV DC (P)</u> Naresh Kumar Room - ECE Lab 212 Block II		<u>Adv DSP</u> Sumit Budhiraja Room - ECE LH-410 Block II			<u>INFORMATION THEORY AND CODING</u> Charu Madhu Room - ECE LH-410 Block II		
Friday	<u>DSD -M.E</u> Nidhi Garg Room - ECE LH-410 Block II	<u>INFORMATION THEORY AND CODING</u> Charu Madhu Room - ECE LH-410 Block II	<u>Adv DSP(P)</u> Sumit Budhiraja Room - ECE LAB 414A Block II			<u>FOCS</u> Preeti Singh Room - ECE LH-410 Block II		

Timetable Incharge

Timetable For ME Electronics and Communications (3rd Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		<u>NNE-ME</u> Sunil Agrawal Room - ECE LH-218 block II	<u>Advanced Antenna Systems</u> Jaget Singh Room - ECE Lab 209 Block II					
Tuesday			<u>NNE-ME</u> Sunil Agrawal Room - ECE Lab 212 Block II					
Wednesday		<u>Advanced Antenna Systems</u> Jaget Singh Room - ECE Lab 212 Block II		<u>NNE-ME</u> Sunil Agrawal Room - ECE Lab 213 Block II				
Thursday				<u>Advanced Antenna Systems</u> Jaget Singh Room - ECE LH-410 Block II		<u>NNE-ME</u> Sunil Agrawal		
Friday			<u>Advanced Antenna Systems</u> Jaget Singh Room - ECE Lab 212 Block II					

Timetable Incharge