

**UNIVERSITYINSTITUTE OFENGINEERINGANDTECHNOLOGY PANJAB
UNIVERSITY, CHANDIGARH- 160014, INDIA**

Advertisement

Announcement for PhD Admissions under “Visvesvaraya PhD scheme for Electronics & IT-Phase II” Sponsored by DIC, MeitY, Govt. of India.

Digital India Corporation (DIC), Ministry of Electronics and Information Technology (MeitY), Govt. of India has granted scholarships under the scheme titled “**Visvesvaraya PhD Scheme for Electronics & IT- Phase II**”. The major features of the scheme for the full-time scholars are given below:

Financial Support for Full-time PhD candidates:

- Fellowship: Rs. 38,750/- per month in 1st& 2nd year and @Rs. 43,750/- per month in 3rd, 4th and 5th years of PhD. (support till PhD completion or 5 years whichever is earlier).
- Reimbursement of Rent (RoR): This component is linked with the fellowship of PhD Candidate .The rate of RoR will be provided as per norms. The classification of the cities HRA. HRA is available to candidates who do not reside in the campus.
- Annualcontingencywillalso be provided.

Applications are invited for admission to **PhD programme in Electronics and Communication Engineering (ECE) – (01-Reserved for C2S program)** for Academic Year 2024-25. Candidates are required to contact and take consent of **Prof. Arvind Kumar**, Chief Investigator (Contact number: 9417600579) and **Prof. Naresh Kumar**, Co-Chief Investigator (Contact number: 9216392982) of C2S program at UIET.

Eligibility Criterion: Qualification and admission procedure will be the same as that of regular PhD admissions as per Panjab University rules (<https://rdc.puchd.ac.in/includes/noticeboard/2023/20230602104743-puph.d.guidelines-2023.pdf?202311125633>)

Desirable-Candidates having Master’s degree in Engineering/Technology in the fields of Electronics and Communication/ VLSI Design/ Microelectronics or related discipline with good academic record with GATE score will be preferable. - Knowledge of Microelectronics and VLSI CAD tools (Cadence Virtuoso, Mentor Graphics, Synopsys, Siemens EDA Tools/TCAD/Xilinx), Analog IC Design, MATLAB. - Knowledge of networking/ Linux-based operating system and relevant areas.

Candidates need to apply online at <https://forms.gle/hwtGApxDZabUaLW99> Google form link.

Last date for submission of applications is 27th August 2024. Details are given on the following websites, <https://jobs.puchd.ac.in/list-jobs.php> and https://uiet.puchd.ac.in/?page_id=3175