



## Electronics & Communication Engg. Department, UIET,

**Panjab University Chandigarh-160036**

(Established under the Panjab University Act VII of 1947- enacted by Govt. of India)

(“A++” Grade, NAAC Accredited)

### **Advertisement for the Project Staff** (Purely on Contract Basis)

<b>Last Date for Submitting Application</b>	<del>November 02, 2023</del> <b>November 14, 2023</b>
---	---

<b>Project Title</b>	Design and development of System-on-Chip for Single-Lead Wearable Electrocardiogram (ECG) for medical devices.
<b>Chief Investigator</b>	<b>Prof Arvind Kumar</b> <b>Email : arvind_uiet@yahoo.co.in</b> <b>Mob:-9417600579</b>
<b>Funding Agency</b>	Ministry of Electronics and Information Technology (MeiTY)
<b>Project Duration</b>	3 years and 6 months

<b>Post</b>	<b>Project Associate (PA)</b>
<b>Number of Positions</b>	02
<b>Salary/fellowship Including HRA</b>	Rs 37000/- per month consolidated
<b>Duration</b>	Initially for one year that would be extendable based on the yearly review/appraisal
<b>Eligibility</b>	Candidate should hold a B.Tech./B.E. degree in CSE/ECE/E&C/E&TC/EE/EEE/EI/IT and must have M.Tech./M.E. degree in CSE/ECE/ E&C/E&TC/EE/EEE/EI/IT/ Microelectronics with expertise/experience in VLSI Design/Technology and its Applications. Candidates are required to have a first division (60% or 6.5 CGPA) or its equivalent in UG and PG as an essential qualification.
<b>Required Skill Set</b>	Understanding of project leading/coordination in activities concerning the VLSI and its Applications domain with a strong capacity for working and operating in a flexible, dynamic environment where creative and strategic thinking is required. Proficiency in written and verbal English. Ability to work independently within allocated resources and flexible to work in teams as needed. Must be disciplined, self- motivated, and have the ability to execute activities, independently. Excellent organizational and time management skills.
<b>Roles and Responsibilities</b>	To assist the Project Team and Project Lead in coordinating the project activities under the defined project viz. managing project laboratory, other project-related academic activities, and any other activities as defined by Institute Project Investigators from time to time.

<b>Application Process</b>	<p>The applicants should apply through Google form link <a href="https://forms.gle/ETYBa6wPynLYAm3A">https://forms.gle/ETYBa6wPynLYAm3A</a></p> <p>Candidates should upload a <b>single pdf file containing a complete CV (including official transcripts, email address, cell phone number, and other details)</b> in the Google form itself.</p> <p>After submitting all details, candidates are advised to send one copy by email also to <a href="mailto:arvind_uiet@yahoo.co.in">arvind_uiet@yahoo.co.in</a>. The subject of the email should be: <i>Application for Project Staff for Chip2Startup Program</i></p> <p>For any queries and information, please feel free to contact</p> <p><b>Prof. Naresh Kumar</b>  <b>Co-Chief Investigator- C2S Project</b>  <b>ECE, UIET ,</b>  <b>Panjab University Chandigarh</b>  <b>Email <a href="mailto:naresh_uiet@yahoo.com">naresh_uiet@yahoo.com</a></b>  <b>Mob:-9216392982</b></p>
----------------------------	--

**Applicants, please note:**

1. The applicants will be responsible for the authenticity of the information provided in support of the experience claimed and other documents.
2. Merely possessing the prescribed qualification does not ensure that the candidate will be called for an Interview. The candidates may be shortlisted based on merit and need for the project.
3. The date and time of the interview will be informed to the shortlisted candidates through email.
4. No TA/DA will be paid for appearing in the interview.
5. Applications received after the last date will not be considered.
6. Only shortlisted candidates will be intimated about the selection process through e-mail.
7. Candidates shall bring along with them the original certificates at the time of the selection process for verification.
8. Engagement of the above staff will be purely on a temporary basis for the duration of the project only
9. Staff engaged will have no claim for regular appointment in the University on the termination of the project.
10. The Institute reserves the right not to fill up the post(s), cancel the advertisement in whole or in part, without assigning any reason, and the decision of the Administration of the University, in this regard, shall be final.
11. The qualification and experience may be relaxed by the Institute for exceptional candidates.

\*\*\*



## Electronics & Communication Engg. Department, UIET,

**Panjab University Chandigarh-160036**

(Established under the Panjab University Act VII of 1947- enacted by Govt. of India)

("A++" Grade, NAAC Accredited)

### **Advertisement for the Project Staff** **(Purely Contract Basis)**

<b>Last Date for Submitting Application</b>	<del>November 02, 2023</del> <b>November 14, 2023</b>
---	---

<b>Project Title</b>	Design and development of System-on-Chip for Single-Lead Wearable Electrocardiogram (ECG) for medical devices.
<b>Chief Investigator</b>	<b>Dr. Arvind Kumar</b> <b>Email arvind_uiet@yahoo.co.in</b> <b>Mob:-9417600579</b>
<b>Funding Agency</b>	Ministry of Electronics and Information Technology (MeiTY)
<b>Project Duration</b>	3 years and 6 months

<b>Post</b>	<b>Senior Project Associate (SPA)</b>
<b>Number of Positions</b>	01
<b>Salary/fellowship Including HRA</b>	Rs 50000/- per month consolidated
<b>Duration</b>	Initially for one year that would be extendable based on the yearly review/appraisal
<b>Eligibility</b>	Candidate should hold a B.Tech./B.E. degree in CSE/ECE/E&C/E&TC/EE/EEE/EI/IT and must have M.Tech./M.E. degree in CSE/ECE/ E&C/E&TC/EE/EEE/EI/IT/ Microelectronics with expertise/experience in VLSI Design/Technology and its Applications. Candidates are required to have a first division (60% or 6.5 CGPA) or its equivalent in UG and PG as an essential qualification. Priority will be given to individuals who possess a Ph.D degree or have submitted their Ph.D. thesis in the field of VLSI Design/Technology and its Applications.
<b>Required Skill Set</b>	Understanding of project leading/coordination in activities concerning the VLSI and its Applications domain with a strong capacity for working and operating in a flexible, dynamic environment where creative and strategic thinking is required. Proficiency in written and verbal English. Ability to work independently within allocated resources and flexible to work in teams as needed. Must be disciplined, self- motivated, and have the ability to execute activities, independently. Excellent organizational and time management skills.

<b>Roles and Responsibilities</b>	Leading and coordinating the project activities under the defined project and concerned work themes. Coordination and steering of various activities for verticals such as Formal Programme and academic activities chiefly (but not limited to)
-----------------------------------	--

	Knowledge Creation through IPR (including patents and research papers), Workshops/ Conferences, and National Competitions etc., under the guidance and in coordination with defined Committees/Groups. Document management including collection, compilation, and report. Preparation of Data and Information for Reports etc. Information dissemination across the agencies and monitoring and steering their progress under the guidance of CI/Co-CIs. Preparing reports, records, documents etc., for presenting information and data. Ensuring timely completion of activities in compliance with defined guidelines and protocols. Query handling, reporting, and facilitating resolution as per fellow Institutes' requirements. Any other activities as defined by the Institute, Project Investigators from time to time.
<b>Application Process</b>	<p>The applicants should apply through Google form link <a href="https://forms.gle/ETYYBa6wPynLYAm3A">https://forms.gle/ETYYBa6wPynLYAm3A</a></p> <p>Candidates should upload a <b>single pdf file containing a complete CV (including official transcripts, email address, cell phone number, and other details)</b> in the Google form itself.</p> <p>After submitting all details, candidates are advised to send one copy by email also to <a href="mailto:arvind_uiet@yahoo.co.in">arvind_uiet@yahoo.co.in</a>. The subject of the email should be: <i>Application for Project Staff for Chip2Startup Program</i></p> <p>For any queries and information, please feel free to contact</p> <p><b>Prof. Naresh Kumar</b>  <b>Co-Chief Investigator- C2S Project</b>  <b>ECE, UIET,</b>  <b>Panjab University Chandigarh</b>  <b>Email <a href="mailto:naresh_uiet@yahoo.com">naresh_uiet@yahoo.com</a></b>  <b>Mob:-9216392982</b></p>

**Applicants, please note:**

1. The applicants will be responsible for the authenticity of the information provided in support of the experience claimed and other documents.
2. Merely possessing the prescribed qualification does not ensure that the candidate will be called for an Interview. The candidates may be shortlisted based on merit and need for the project.
3. The date and time of the interview will be informed to the shortlisted candidates through email.
4. No TA/DA will be paid for appearing in the interview.
5. Applications received after the last date will not be considered.

6. Only shortlisted candidates will be intimated about the selection process through e-mail.
7. Candidates shall bring along with them the original certificates at the time of the selection process for verification.
8. Engagement of the above staff will be purely on a temporary basis for the duration of the project only
9. Staff engaged will have no claim for regular appointment in the University on the termination of the project.
10. The Institute reserves the right not to fill up the post(s), cancel the advertisement in whole or in part, without assigning any reason, and the decision of the Administration of the University, in this regard, shall be final.
11. The qualification and experience may be relaxed by the Institute for exceptional candidates.