Exam.Code:0927
Sub. Code: 6573

2122

B.E. (Electronics and Communication Engineering)
Third Semester
EC-303: Microprocessor and Applications

Time allowed: 3 Hours

Max. Marks: 50

NOTE: *Attempt __five__ questions in all, including Question No. I which is compulsory and selecting two questions from each Unit.*

x-x-x

I. Explain the following:-

a) Interrupt latency in microprocessors

b) Direct memory access

c) Trap Interrupt

d) Role of BIU in 8086 processor

e) Name any two arithmetic instructions

f) Addressing Modes

g) Memory mapped

h) Role of Buses

i) Name control buses

j) ISR

(10x1)

UNIT - I

II. Explain the terms "random access" and "sequential access". Identify the type of access in the following:

a) RAM

b) EPROM

(10)

III. Write an 8085 assembly language program, which takes the data from memory location X, and divides this byte by 8, and stores the result at memory location Y.

(10)

IV. a) Explain the architecture of 8085 microprocessor in detail.

b) Explain the pin diagram of 8085 microprocessor.

(2x5)

UNIT - II

V. a) Explain the functions of Counters and timers in processors.

b) Explain in detail the Johnson Counter.

(2x5)

P.T.O.

VI. a) What are vectored interrupts in 8085? Explain each interrupt in brief.

b) How does the 8259A differentiate between 8- bit and 16-bit microprocessors?

(2x5)

VII. a) Explain the concept of Stack and Subroutine in 8085 microprocessor.

b) Explain with the help of diagram the 8086 microprocessor.   (2x5)

x-x-x