

1108
B.E., (Electrical and Electronics Engineering)
Fourth Semester
EE-405: Microprocessor and Interfacing
(May – 2017)

Time allowed: 3 Hours

Max. Marks: 50

NOTE: Attempt five questions in all, including Question No. 1 which is compulsory and selecting two questions from each Unit.

x-x-x

I. Attempt the following:-

- a) What is the memory capacity and word size of 8085?
- b) What are Vectored Interrupts?
- c) What is the function of ALE?
- d) Differentiate between LHLD FAOOH and LXI 1234H.
- e) What is the function of status signals in 8085? (5x2)

UNIT – I

- II. a) Give the Programming Model of 8085.
- b) Differentiate between Peripheral Mapping and Memory Mapping (6,4)
- III. a) For a set of thirty data bytes stored at address 2050H onwards, WAP to add all positive numbers and neglect all negative numbers store. The resulting sum at memory location 2080H and carry at 2081H respectively.
- b) Discuss the Addressing Modes of 8085. (6,4)
- IV. a) Draw the Timing Diagram for the instruction : OUT F4H
- b) A string of 16 data bytes is stored starting from memory location 3000H .Write an assembly language program to Ignore all blanks (00), and relocate the Remaining bytes at address 3200H. (4,6)

UNIT – II

- V. a) Give block diagram of Programmable Peripheral interface 8255
- b) Discuss the control word for BSR and I/O Modes (5,5)

P.T.O.

(2)

- VI. a) Discuss stepper motor interfacing with 8085.
b) Explain interfacing of a seven segment display with 8085. (5,5)

VII. Write short notes on any three of the following:-

- a) Architecture of 8086
b) Keyboard Debouncing
c) Nested Subroutines
d) Tri-state devices (3,3,4)

x-x-x