8/22/2019 CSE1.htm

Timetable For BE Computer Science (Sec-I) (1st Semester)

Timetable	For BE C	omputer	Science	(Sec-1) (
Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday	Digital Electronics and Logic Design Sunaina Gulati Room - LH-13 Block II	Quantum Physics Shuchi Gupta Room - LH-13 Block II	Basics of Electrical and Electronics Engineering Charu Madhu Room - LH-13 Block II	Calculus Minto Rattan Room - LH-13 Block II	Calculus Group 3 Minto Rattan Room - LH-13 Block II			
Tuesday	Quantum Physics Shuchi Gupta Room - LH-13		Electronics and Logic Design Room - Sunaina Gulati	Calculus Minto Rattan Room - LH-13 Block II		Quantum Physics (P) Group 1 Shuchi Gupta Room - Physics Lab-I 306 Block II		
		Block II	Room - LH-13 Block II			Calculus Group 2 Minto Rattan Room - LH-14 Block II	Electronics E Gro Charu	ectrical and ngineering (P) up 2 Madhu ab 415 Block II
						Digital Electronics and Logic Design Group 3 Lokesh Miglani Room - LH-15 Block II	Digital Electronics and Logic Design (P). Group 3 Room - CSE Lab 412 Block I	
Wednesday	Gro Room - CSE L	u <u>n (P)</u> up 1 ab 412 Block I	Calculus Group 1 Minto Rattan Room - LH-13 Block II	Quantum Physics Shuchi Gupta Room - LH-13 Block II	Basics of Electrical and Electronics Engineering Charu Madhu Room - LH-13			
	<u>Quantum Physics (P)</u> Group 2 Shuchi Gupta Room - Physics Lab-1 306 Block II				Block II			
	Basics of Electrical and Electronics Engineering (P) Group 3 Charu Madhu Room - ECE Lab 415 Block II							
Thursday	Calculus Minto Rattan Room - LH-13 Block II		Electronics E Gro Charu	Basics of Electrical and Electronics Engineering (P) Group 1 Charu Madhu Room - ECE Lab 211 Block II				
		<u>Desig</u> Gro	onics and Logic gn (P) up 2 ab 412 Block I	Digital Electronics and Logic Design Group 2 Sunaina Gulati Room - CSE Lab	Room - LH-14 Block II			
	<u>Quantum Physics (</u> Group 3 Shuchi Gupta Room - Physics Lab-II 307			412 Block I (P)				
Friday	Digital Electronics and Logic Design Sunaina Gulati Room - LH-13 Block II	Quantum Physics Shuchi Gupta Room - LH-13 Block II	Basics of Electrical and Electronics Engineering Charu Madhu Room - LH-13 Block II	Calculus Minto Rattan Room - LH-13 Block II				

Timetable Incharge

8/22/2019 CSE2.htm

Timetable For BE Computer Science (Sec-II) (1st Semester)

Days	9 - 10	10 - 11	11 - 12	12 - 13	13 - 14	14 - 15	15 - 16	16 - 17
Monday		Calculus Minto Rattan Room - LH-15 Block II	Quantum Physics Geetu Room - LH-15 Block II	<u>Digital</u> Electronics and <u>Logic Design</u> Lokesh Miglani Room - LH-15 Block II		Quantum Physics (P) Group 1 Geetu Room - Physics Lab-II 307 Block II Calculus Group 2 Minto Rattan Room - LH-15 Block II Basics of Electrical and Electronics Engineering (P). Group 3 Group 3 Minto Rattan Room - EVEL Lab 211 Block II		
Tuesday		Calculus Minto Rattan Room - LH-15 Block II	Quantum Physics Geetu Room - LH-15 Block II	Basics of Electrical and Electronics Engineering Vijay Room - LH-15 Block II				
Wednesday		Calculus Minto Rattan Room - LH-16 Block II	Quantum Physics Geetu Room - LH-16 Block II	<u>Digital</u> <u>Electronics and</u> <u>Logic Design</u> <u>Lokesh Miglani</u> Room - LH-16 Block II		Calculus Group 1 Minto Rattan Room - LH-15 Block II Digital Electronics and Logic Group 1 Room - CSE Lab 412 Block I Block II Basics of Electrical and Electronics Engineering (P) Group 2 Lokesh Miglani Room - 406 Block II Quantum Physics (P) Group 3 Geetu Room - Physics Lab-II 307 Block II		
Thursday		Quantum Physics Geetu Room - LH-16 Block II	Calculus Minto Rattan Room - LH-16 Block II	<u>Digital</u> <u>Electronics and</u> <u>Logic Design</u> Lokesh Miglani Room - LH-16 Block II	Basics of Electrical and Electronics Engineering Vijay Room - LH-16 Block II			
Friday	Digital Electronics and Logic Design Group 3 Lokesh Miglani Room - CSE Lab 412 Block I	Room - l <u>Digital Electro</u> <u>Desig</u>	Electronics E Gre Vi Room - ECE L uantum Physics . Group 2 Geetu Physics Lab-II 307 mics and Logic n_(P) up 3		Basics of Electrical and Electronics Engineering Vijay Room - LH-13 Block II	<u>Digital</u> <u>Electronics and</u> <u>Logic Design</u> Group I Lokesh Miglani Room - LH-13 Block II		

Timetable Incharge